



LB-889

APPLICATION OF LINEAR ACTIVE

FOUR-TERMINAL NETWORK

THEORY TO TRANSISTORS

RADIO CORPORATION OF AMERICA  
RCA LABORATORIES DIVISION  
INDUSTRY SERVICE LABORATORY

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# Application of Linear Active Four-Terminal Network Theory to Transistors

## Introduction

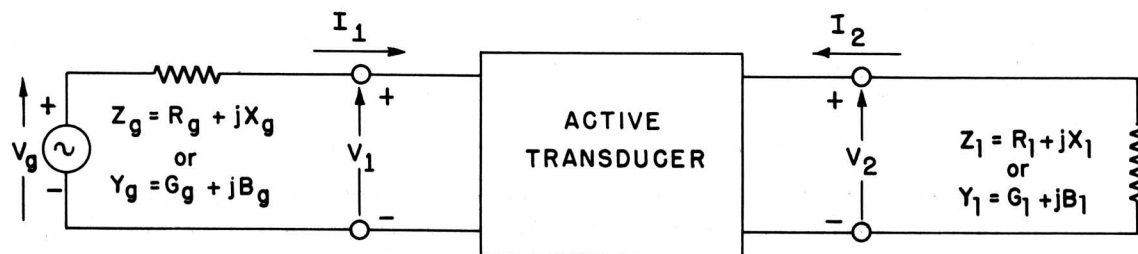
With the advent of transistors, considerably greater usage has been made of linear active four-terminal network theory. Some of this usage has been complicated because of differences in terminology. This bulletin is an attempt to express, in a unified system of nomenclature, the application of linear active four-terminal network theory to transistors. It is hoped that this will serve as a step towards a standard system of nomenclature.

Section I deals with the general properties of a linear active four-terminal network. Section II is devoted to the tabulation of circuits associated with transistor applications. For those transistor circuit properties not tabulated in this section, reference should be made to the appropriate equations in Section I. Several examples of the application of material in Sections I and II to transistor circuits are given in Section III.

## Section I—Linear Active Four-Terminal Networks and Equations

In general, any linear active four-terminal network is characterized by two equations which interrelate the currents and voltages at its input and output terminals. These equations may be written in either nodal or loop equation form.

### A. Circuit With Associated Generator And Load, And Equations



#### NODAL EQUATIONS

$$I_1 = y_{11}V_1 + y_{12}V_2$$

$$I_2 = y_{21}V_1 + y_{22}V_2$$

#### LOOP EQUATIONS

$$V_1 = z_{11}I_1 + z_{12}I_2$$

$$V_2 = z_{21}I_1 + z_{22}I_2$$



### B. Definition Of Transducer Parameters

The above circuit equations, which completely describe the small-signal operation of the transducer, also serve as a guide to defining the parameters. Thus, for example, if  $V_2 = 0$ , i.e., the output is shorted, then  $y_{11} = I_1/V_1$  defines  $y_{11}$  as the input admittance when the output is shorted. Continuing in this manner all of the parameters may be similarly defined.

NODAL	LOOP
$y_{11} = g_{11} + jb_{11}$ = input admittance with output shorted	$z_{11} = r_{11} + jx_{11}$ = input impedance with output open
$y_{12} = g_{12} + jb_{12}$ = reverse transfer (feedback) admittance with input shorted	$z_{12} = r_{12} + jx_{12}$ = reverse transfer (feedback) impedance with input open
$y_{21} = g_{21} + jb_{21}$ = forward transfer admittance with output shorted	$z_{21} = r_{21} + jx_{21}$ = forward transfer impedance with output open
$y_{22} = g_{22} + jb_{22}$ = output admittance with input shorted	$z_{22} = r_{22} + jx_{22}$ = output impedance with input open

### C. Transformation Equations

In general, the parameters within each pair of equations are independent of each other. The two sets of parameters, however, are related by the following transformation equations:

NODAL	LOOP
$y_{11} = z_{22}/\Delta_z$	$z_{11} = y_{22}/\Delta_y$
$y_{12} = -z_{12}/\Delta_z$	$z_{12} = -y_{12}/\Delta_y$
$y_{21} = -z_{21}/\Delta_z$	$z_{21} = -y_{21}/\Delta_y$
$y_{22} = z_{11}/\Delta_z$	$z_{22} = y_{11}/\Delta_y$
$\Delta_z = \begin{vmatrix} z_{11} & z_{12} \\ z_{21} & z_{22} \end{vmatrix}$	$\Delta_y = \begin{vmatrix} y_{11} & y_{12} \\ y_{21} & y_{22} \end{vmatrix}$

### D. Definition Of Amplification Factors

In addition to the parameters already discussed, it is convenient to define various amplification factors which serve as indexes of performance. Since the network may in general be bilateral, each factor must be defined in the reverse as well as in the forward direction.

#### Current Amplification Factors

The current amplification factor is defined as the ratio of the negative of the current at one pair of shorted terminals to the current introduced at the other pair of terminals.

$\alpha_{21}$  = *forward* current amplification factor

= ratio of the negative current at the shorted *output* terminals to the current introduced at the *input* terminals.

$\alpha_{12}$  = *reverse* current amplification factor

= ratio of the negative current at the shorted *input* terminals to the current introduced at the *output* terminals.

#### Voltage Amplification Factors

The voltage amplification factor is defined as the ratio of the voltage at one pair of open terminals to the voltage applied to the other pair of terminals. Note that voltage amplification as defined in reference (6) is only the magnitude of voltage amplification as defined here.

$\mu_{21}$  = *forward* voltage amplification factor

= ratio of the voltage at the open *output* terminals to the voltage introduced at the *input* terminals.

$\mu_{12}$  = *reverse* voltage amplification factor

= ratio of the voltage at the open *input* terminals to the voltage introduced at the *output* terminals.

#### Power Amplification Factors

The power amplification factor is defined as the maximum power amplification in a given direction when the transfer impedance or the transfer admittance in the opposite direction is zero. This definition is introduced for the first time since it is believed that the forward power amplification factor in particular may be more useful than either the forward current or the forward voltage amplification factor as a single index of performance.

$\phi_{21}$  = *forward* power amplification factor

= maximum power amplification from the *input* to the *output* terminals when the *reverse* transfer (feedback) impedance or the *reverse* transfer (feedback) admittance is zero.

$\phi_{12}$  = *reverse* power amplification factor

= maximum power amplification from the *output* to the *input* terminals when the *forward* transfer impedance or the *forward* transfer admittance is zero.

#### E. Equation For Amplification Factors

The amplification factors can be readily determined in terms of the impedance or admittance parameters already defined.

##### NODAL

$$\alpha_{21} = -y_{21}/y_{11}$$

$$\alpha_{12} = -y_{12}/y_{22}$$

$$\mu_{21} = -y_{21}/y_{22}$$

$$\mu_{12} = -y_{12}/y_{11}$$

##### LOOP

$$\mu_{21} = z_{21}/z_{11}$$

$$\mu_{12} = z_{12}/z_{22}$$

$$\alpha_{21} = z_{21}/z_{22}$$

$$\alpha_{12} = z_{12}/z_{11}$$

$$\varphi_{21} = \frac{|y_{21}|^2}{4g_{11}g_{22}}$$

$$\varphi_{12} = \frac{|y_{12}|^2}{4g_{11}g_{22}}$$

$$\varphi_{21} = \frac{|z_{21}|^2}{4r_{11}r_{22}}$$

$$\varphi_{12} = \frac{|z_{12}|^2}{4r_{11}r_{22}}$$

### F. Input And Output Impedances

The input and output impedances of the transducer under circuit conditions are, of course, dependent on the actual load and source impedances respectively. These impedances are readily determined by conventional analysis and are given by:

#### NODAL

$$y_i = g_i + jb_i = \text{input admittance}$$

$$= y_{11} - \frac{y_{12}y_{21}}{y_{22} + Y_1}$$

$$y_o = g_o + jb_o = \text{output admittance}$$

$$= y_{22} - \frac{y_{12}y_{21}}{y_{11} + Y_g}$$

#### LOOP

$$z_i = r_i + jx_i = \text{input impedance}$$

$$= z_{11} - \frac{z_{12}z_{21}}{z_{22} + Z_1}$$

$$z_o = r_o + jx_o = \text{output impedance}$$

$$= z_{22} - \frac{z_{12}z_{21}}{z_{11} + Z_g}$$

### G. Amplification Under Arbitrary Conditions

The values of amplification for arbitrary values of generator and load impedances are given by the following equations:

#### NODAL

Forward Current Amplification

$$= \frac{y_{21}Y_1}{y_{11}(y_{22} + Y_1) - y_{12}y_{21}}$$

Reverse Current Amplification

$$= \frac{y_{12}Y_g}{y_{22}(y_{11} + Y_g) - y_{12}y_{21}}$$

Forward Voltage Amplification

$$= - \frac{y_{21}}{y_{22} + Y_1}$$

Reverse Voltage Amplification

$$= - \frac{y_{12}}{y_{11} + Y_g}$$

Forward Power Amplification

$$= \left| \frac{y_{21}}{y_{22} + Y_1} \right|^2 \frac{G_1}{g_i}$$

#### LOOP

Forward Voltage Amplification

$$= \frac{z_{21}Z_1}{z_{11}(z_{22} + Z_1) - z_{12}z_{21}}$$

Reverse Voltage Amplification

$$= \frac{z_{12}Z_g}{z_{22}(z_{11} + Z_g) - z_{12}z_{21}}$$

Forward Current Amplification

$$= - \frac{z_{21}}{z_{22} + Z_1}$$

Reverse Current Amplification

$$= - \frac{z_{12}}{z_{11} + Z_g}$$

Forward Power Amplification

$$= \left| \frac{z_{21}}{z_{22} + Z_1} \right|^2 \frac{R_1}{r_i}$$

Reverse Power Amplification

$$= \left| \frac{y_{12}}{y_{11} + Y_g} \right|^2 \frac{G_g}{g_o}$$

Reverse Power Amplification

$$= \left| \frac{z_{12}}{z_{11} + Z_g} \right|^2 \frac{R_g}{r_o}$$

#### H. Equation For Conjugate Impedance Match

The maximum power amplification is obtained when the generator and the load impedances are the complex conjugate of the input and output impedances respectively.

NODAL

Let:

$$G_N = \frac{g_{12}g_{21} - b_{12}b_{21}}{g_{11}g_{22}}$$

$$B_N = \frac{b_{12}g_{21} - b_{21}g_{12}}{2g_{11}g_{22}}$$

Then:

$$\begin{aligned} \text{Input Admittance} \\ = g_{11} \sqrt{1 - G_N - B_N^2} + j(b_{11} - g_{11}B_N) \end{aligned}$$

Output Admittance

$$= g_{22} \sqrt{1 - G_N - B_N^2} + j(b_{22} - g_{22}B_N)$$

Forward Power Amplification

$$= \frac{|y_{21}|^2}{g_{11}g_{22} [2 + 2\sqrt{1 - G_N - B_N^2} - G_N]}$$

Reverse Power Amplification

$$= \frac{|y_{12}|^2}{g_{11}g_{22} [2 + 2\sqrt{1 - G_N - B_N^2} - G_N]}$$

LOOP

Let:

$$R_N = \frac{r_{12}r_{21} - x_{12}x_{21}}{r_{11}r_{22}}$$

$$X_N = \frac{x_{12}r_{21} - x_{21}r_{12}}{2r_{11}r_{22}}$$

Then:

$$\begin{aligned} \text{Input Impedance} \\ = r_{11} \sqrt{1 - R_N - X_N^2} + j(x_{11} - r_{11}X_N) \end{aligned}$$

Output Impedance

$$= r_{22} \sqrt{1 - R_N - X_N^2} + j(x_{22} - r_{22}X_N)$$

Forward Power Amplification

$$= \frac{|z_{21}|^2}{r_{11}r_{22} [2 + 2\sqrt{1 - R_N - X_N^2} - R_N]}$$

Reverse Power Amplification

$$= \frac{|z_{12}|^2}{r_{11}r_{22} [2 + 2\sqrt{1 - R_N - X_N^2} - R_N]}$$

#### I. Equations For Image Impedance Match

The transducer is image impedance matched when the generator and load impedances are equal to the input and output impedances respectively.

NODAL

Input Admittance

$$= y_{11} \sqrt{1 - a_{12}a_{21}}$$

Output Admittance

$$= y_{22} \sqrt{1 - a_{12}a_{21}}$$

LOOP

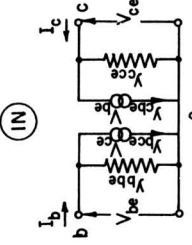
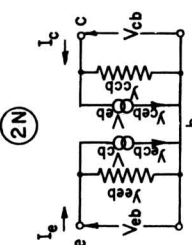
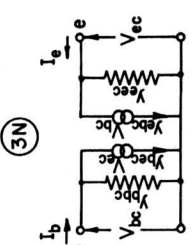
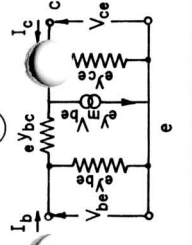
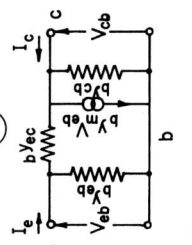
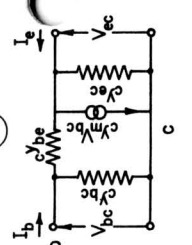
Input Impedance

$$= z_{11} \sqrt{1 - a_{12}a_{21}}$$

Output Impedance

$$= z_{22} \sqrt{1 - a_{12}a_{21}}$$



EQUATIONS	NODAL DERIVED EQUIVALENT CIRCUITS			LOOP DERIVED EQUIVALENT CIRCUITS		
	COMMON EMITTER	COMMON BASE	COMMON COLLECTOR	COMMON EMITTER	COMMON BASE	COMMON COLLECTOR
TWO - GEN	$I_b = Y_{bbe} V_{be} + Y_{bce} V_{ce}$ $I_c = Y_{cbe} V_{be} + Y_{cce} V_{ce}$	$I_e = Y_{eeb} V_{eb} + Y_{ecb} V_{cb}$ $I_o = Y_{ceb} V_{eb} + Y_{ccb} V_{cb}$	$I_b = Y_{bbc} V_{bc} + Y_{bec} V_{ec}$ $I_e = Y_{ebc} V_{bc} + Y_{eec} V_{ec}$			
	$d_{cb} = -\frac{Y_{cbe}}{Y_{bbe}}$ $\mu_{cb} = -\frac{Y_{cbe}}{Y_{cce}}$ $\phi_{cb} = \frac{Y_{cbe}^2}{Y_{bbe} Y_{cce}}$	$d_{ec} = -\frac{Y_{ceb}}{Y_{eeb}}$ $\mu_{ec} = -\frac{Y_{ceb}}{Y_{ccb}}$ $\phi_{ce} = \frac{Y_{ceb}^2}{Y_{eeb} Y_{ccb}}$	$d_{eb} = -\frac{Y_{bec}}{Y_{bbc}}$ $\mu_{eb} = -\frac{Y_{bec}}{Y_{yec}}$ $\phi_{be} = \frac{Y_{bec}^2}{Y_{bbc} Y_{yec}}$	$d_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\mu_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\phi_{cb} = \frac{Z_{cbe}^2}{Z_{bbe} Z_{bbe}}$	$d_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\mu_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\phi_{ce} = \frac{Z_{ceb}^2}{Z_{eeb} Z_{eeb}}$	$d_{eb} = \frac{Z_{bec}}{Z_{bbc}}$ $\mu_{eb} = \frac{Z_{bec}}{Z_{yec}}$ $\phi_{be} = \frac{Z_{bec}^2}{Z_{bbc} Z_{yec}}$
AMP. FACTORS	$d_{cb} = -\frac{Y_{cbe}}{Y_{bbe}}$ $\mu_{cb} = -\frac{Y_{cbe}}{Y_{cce}}$ $\phi_{cb} = \frac{Y_{cbe}^2}{Y_{bbe} Y_{cce}}$	$d_{ec} = -\frac{Y_{ceb}}{Y_{eeb}}$ $\mu_{ec} = -\frac{Y_{ceb}}{Y_{ccb}}$ $\phi_{ce} = \frac{Y_{ceb}^2}{Y_{eeb} Y_{ccb}}$	$d_{eb} = -\frac{Y_{bec}}{Y_{bbc}}$ $\mu_{eb} = -\frac{Y_{bec}}{Y_{yec}}$ $\phi_{be} = \frac{Y_{bec}^2}{Y_{bbc} Y_{yec}}$	$d_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\mu_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\phi_{cb} = \frac{Z_{cbe}^2}{Z_{bbe} Z_{bbe}}$	$d_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\mu_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\phi_{ce} = \frac{Z_{ceb}^2}{Z_{eeb} Z_{eeb}}$	$d_{eb} = \frac{Z_{bec}}{Z_{bbc}}$ $\mu_{eb} = \frac{Z_{bec}}{Z_{yec}}$ $\phi_{be} = \frac{Z_{bec}^2}{Z_{bbc} Z_{yec}}$
TRANSFORMATION EQUATIONS	$d_{cb} = -\frac{Y_{cbe}}{Y_{bbe}}$ $\mu_{cb} = -\frac{Y_{cbe}}{Y_{cce}}$ $\phi_{cb} = \frac{Y_{cbe}^2}{Y_{bbe} Y_{cce}}$	$d_{ec} = -\frac{Y_{ceb}}{Y_{eeb}}$ $\mu_{ec} = -\frac{Y_{ceb}}{Y_{ccb}}$ $\phi_{ce} = \frac{Y_{ceb}^2}{Y_{eeb} Y_{ccb}}$	$d_{eb} = -\frac{Y_{bec}}{Y_{bbc}}$ $\mu_{eb} = -\frac{Y_{bec}}{Y_{yec}}$ $\phi_{be} = \frac{Y_{bec}^2}{Y_{bbc} Y_{yec}}$	$d_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\mu_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\phi_{cb} = \frac{Z_{cbe}^2}{Z_{bbe} Z_{bbe}}$	$d_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\mu_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\phi_{ce} = \frac{Z_{ceb}^2}{Z_{eeb} Z_{eeb}}$	$d_{eb} = \frac{Z_{bec}}{Z_{bbc}}$ $\mu_{eb} = \frac{Z_{bec}}{Z_{yec}}$ $\phi_{be} = \frac{Z_{bec}^2}{Z_{bbc} Z_{yec}}$
ONE - GEN	$I_b = Y_{bbe} V_{be} + Y_{bce} V_{ce}$ $I_c = Y_{cbe} V_{be} + Y_{cce} V_{ce}$	$I_e = Y_{eeb} V_{eb} + Y_{ecb} V_{cb}$ $I_o = Y_{ceb} V_{eb} + Y_{ccb} V_{cb}$	$I_b = Y_{bbc} V_{bc} + Y_{bec} V_{ec}$ $I_e = Y_{ebc} V_{bc} + Y_{eec} V_{ec}$			
	$d_{cb} = -\frac{Y_{cbe}}{Y_{bbe}}$ $\mu_{cb} = -\frac{Y_{cbe}}{Y_{cce}}$ $\phi_{cb} = \frac{Y_{cbe}^2}{Y_{bbe} Y_{cce}}$	$d_{ec} = -\frac{Y_{ceb}}{Y_{eeb}}$ $\mu_{ec} = -\frac{Y_{ceb}}{Y_{ccb}}$ $\phi_{ce} = \frac{Y_{ceb}^2}{Y_{eeb} Y_{ccb}}$	$d_{eb} = -\frac{Y_{bec}}{Y_{bbc}}$ $\mu_{eb} = -\frac{Y_{bec}}{Y_{yec}}$ $\phi_{be} = \frac{Y_{bec}^2}{Y_{bbc} Y_{yec}}$	$d_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\mu_{cb} = \frac{Z_{cbe}}{Z_{bbe}}$ $\phi_{cb} = \frac{Z_{cbe}^2}{Z_{bbe} Z_{bbe}}$	$d_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\mu_{ec} = \frac{Z_{ceb}}{Z_{eeb}}$ $\phi_{ce} = \frac{Z_{ceb}^2}{Z_{eeb} Z_{eeb}}$	$d_{eb} = \frac{Z_{bec}}{Z_{bbc}}$ $\mu_{eb} = \frac{Z_{bec}}{Z_{yec}}$ $\phi_{be} = \frac{Z_{bec}^2}{Z_{bbc} Z_{yec}}$

## TRANSISTOR CIRCUITS AND EQUATIONS

$$\text{Forward current amplification} = - \frac{\alpha_{21}}{1 + \sqrt{1 - \alpha_{12}\alpha_{21}}}$$

$$\text{Reverse current amplification} = - \frac{\alpha_{12}}{1 + \sqrt{1 - \alpha_{12}\alpha_{21}}}$$

$$\text{Forward voltage amplification} = \frac{\mu_{21}}{1 + \sqrt{1 - \alpha_{12}\alpha_{21}}}$$

$$\text{Reverse voltage amplification} = \frac{\mu_{12}}{1 + \sqrt{1 - \alpha_{12}\alpha_{21}}}$$

If all transducer parameters are real,

$$\text{Forward power amplification} = \frac{\alpha_{21}\mu_{21}}{(1 + \sqrt{1 - \alpha_{12}\alpha_{21}})^2}$$

$$\text{Reverse power amplification} = \frac{\alpha_{12}\mu_{12}}{(1 + \sqrt{1 - \alpha_{12}\alpha_{21}})^2}$$

#### J. Equations For Iterative Impedance Match

The transducer is iterative impedance matched if it is an element of an infinite number (or the equivalent thereof) of cascaded identical transducers.

NODAL

Let:

$$y_m = \frac{y_{11} + y_{22}}{2}$$

Then:

Input Admittance

$$= -y_{22} + y_m \left[ 1 + \sqrt{1 - \frac{y_{12}y_{21}}{y_m}} \right]$$

Output Admittance

$$= -y_{11} + y_m \left[ 1 + \sqrt{1 - \frac{y_{12}y_{21}}{y_m}} \right]$$

LOOP

Let:

$$z_m = \frac{z_{11} + z_{22}}{2}$$

Then:

Input Impedance

$$= -z_{22} + z_m \left[ 1 + \sqrt{1 - \frac{z_{12}z_{21}}{z_m}} \right]$$

Output Impedance

$$= -z_{11} + z_m \left[ 1 + \sqrt{1 - \frac{z_{12}z_{21}}{z_m}} \right]$$

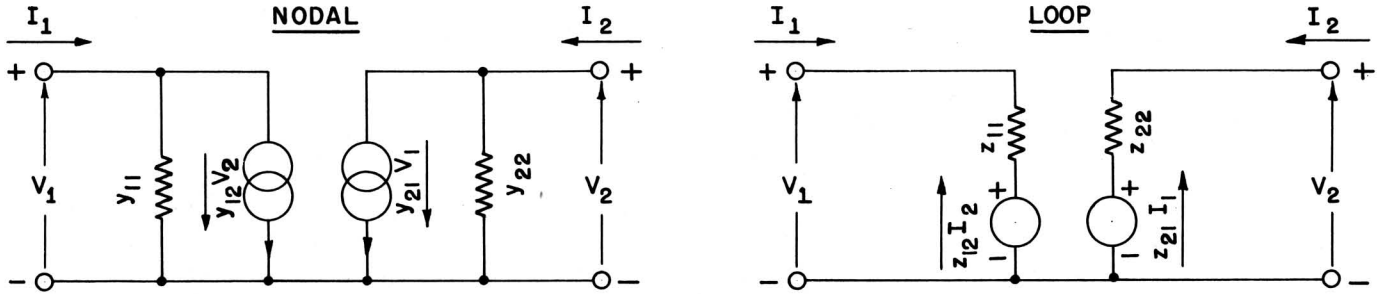
The equations in Section IG may be used to obtain the various circuit amplifications.

#### K. Equivalent Circuits

While formal manipulation of the admittance and impedance parameters of a network will result in the correct algebraic expression for whatever quantity is desired, it is nevertheless convenient to be able to express the basic relationships of Section IA in terms of an equivalent circuit.

### Two-Generator Equivalent Circuits

When two generators are used the circuit follows directly from the equations of Section 1A.



### Single-Generator Equivalent Circuits

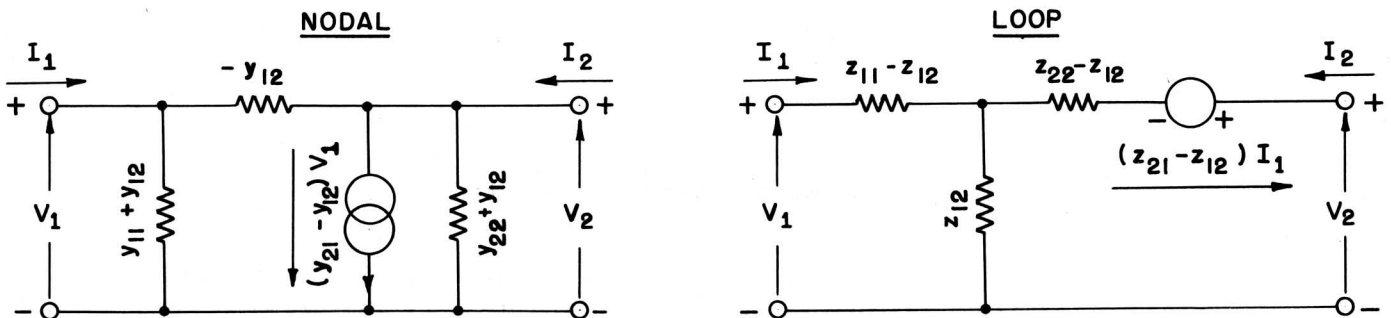
It is often desirable to express the network in terms of a single-generator equivalent circuit. This may be done in several ways. For example,  $z_{21}$  may be separated into two parts:

$$z_{21} = z_{12} + (z_{21} - z_{12}).$$

The basic equations then become

$$\begin{aligned} V_1 &= z_{11}I_1 + z_{12}I_2 \\ V_2 &= z_{12}I_1 + z_{22}I_2 + (z_{21} - z_{12})I_1 \end{aligned}$$

The expressions to the left of the dotted line represent a passive network with a common mutual impedance, and hence is representable by a T network. The remainder of the expression represents a voltage source. Hence the corresponding loop network, shown below, can readily be drawn. For an indication of the numerous other equivalent circuits that can be derived, see reference (3).



Measurement of the actual parameters of a given network may be made by the methods outlined in reference (5).

## Section II—Equivalent Circuits of Transistors

A compilation of the more useful transistor equivalent circuits is given on pages eight and nine. Included with the circuits are the amplification factors and the transformation equations which enable one to go readily from a nodal (or loop) derived equivalent circuit to any other nodal (or loop) derived equivalent circuit. However, in order to go from a nodal to the corresponding loop derived equivalent circuit, or vice versa, the transformation equations of section IC are required.

The notation used in this tabulation was chosen in order to avoid ambiguity among the various circuit parameters. When applied to the two-generator equivalent circuits this notation basically consists of the conventional two letter subscript. To this conventional notation, a third subscript has been added, which designates the common terminal between the input and output circuit. Thus  $z_{bbe}$  is the base self impedance of the transistor in a common-emitter circuit, and  $y_{ceb}$  is the transfer admittance to the collector from the emitter in a common-base circuit. If only one circuit connection is being considered or if no ambiguity arises, the last subscript designating the common electrode may be omitted.

When applied to the single-generator equivalent circuit, a pre-subscript designates the common electrode between the input and output circuits. For the  $\pi$ -equivalent or nodal-derived network, admittances are used with the two post-subscripts designating the terminals between which the admittance is located. Thus  ${}_cy_{be}$  is the admittance between the base and the emitter in a common-collector circuit. For the T-equivalent or loop-derived network there is one node in common with all the elements of the T. Thus only one post-subscript is necessary to designate the terminal to which the impedance is connected. Accordingly  ${}_bz_c$  is the impedance in a common-base circuit between the collector and the center of the T. In both the  $\pi$  and T networks the generator parameter is designated by the subscript m. If only one circuit connection is being considered or if no ambiguity arises, the pre-subscript designating the common electrode may be omitted.

When it is desirable to use admittances in place of impedances, or vice-versa, ambiguity may be avoided by using reciprocal notation. Thus  $1/y_{eeb}$  is the emitter self impedance in the two-generator, common-base, nodal-derived, equivalent circuit, and  $1/{}_ez_m$  is the generator admittance in the single-generator, common-emitter, loop-derived equivalent circuit.

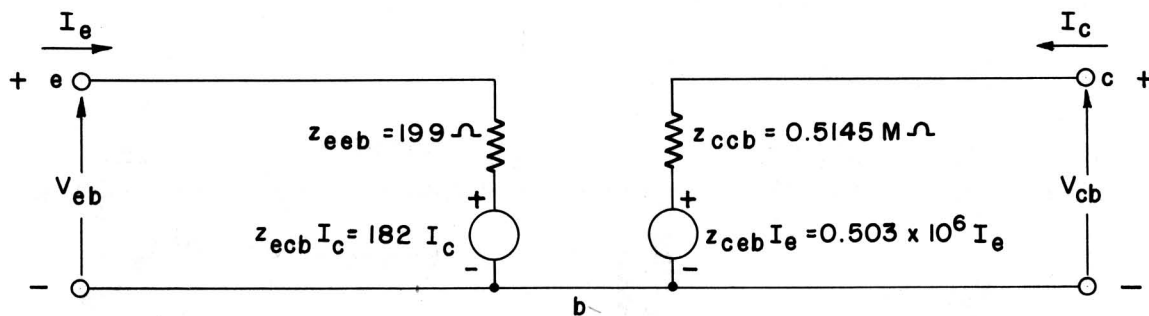
## Section III—Illustrative Calculations

Consider a junction transistor in a common-base circuit for which the following low-frequency open-circuited impedance measurements have been made.

$$z_{11} = z_{eeb} = 199 \text{ ohms} \quad z_{21} = z_{ceb} = 0.503 \text{ megohm.}$$

$$z_{12} = z_{ecb} = 182 \text{ ohms} \quad z_{22} = z_{ccb} = 0.5145 \text{ megohm}$$

Therefore the two-generator equivalent circuit (2L) of Section II is:



Using these data, the amplification factors can be computed from Section IE,

$$\mu_{21} = \mu_{ce} = z_{ceb}/z_{eeb} = 2,530$$

$$\mu_{12} = \mu_{ec} = z_{ecb}/z_{ccb} = 354 \times 10^{-6}$$

$$\alpha_{21} = \alpha_{ce} = z_{ceb}/z_{ccb} = 0.977$$

$$\alpha_{12} = \alpha_{ec} = z_{ecb}/z_{eeb} = 0.915$$

$$\varphi_{21} = \varphi_{ce} = \frac{|z_{ceb}|^2}{4r_{eeb}r_{ccb}} = 620$$

$$\varphi_{12} = \varphi_{ec} = \frac{|z_{ecb}|^2}{4r_{eeb}r_{ccb}} = 81 \times 10^{-6}$$

Since all elements are real, the image match equations of Section II can be used. Thus the input and output match impedances are:

$$\text{Input Impedance} = z_{eeb}\sqrt{1-\alpha_{ec}\alpha_{ce}} = 64.8 \text{ ohms}$$

$$\text{Output Impedance} = z_{ccb}\sqrt{1-\alpha_{ec}\alpha_{ce}} = 167,000 \text{ ohms}$$

and the amplification values for these conditions are:

$$\text{Forward current amplification} = -\frac{\alpha_{ce}}{1+\sqrt{1-\alpha_{ec}\alpha_{ce}}} = -0.737$$

$$\text{Reverse current amplification} = -\frac{\alpha_{ec}}{1+\sqrt{1-\alpha_{ec}\alpha_{ce}}} = -0.690$$

$$\text{Forward voltage amplification} = \frac{\mu_{ce}}{1+\sqrt{1-\alpha_{ec}\alpha_{ce}}} = 1,910$$

$$\text{Reverse voltage amplification} = \frac{\mu_{ec}}{1+\sqrt{1-\alpha_{ec}\alpha_{ce}}} = 267 \times 10^{-6}$$

$$\text{Forward power amplification} = \frac{\alpha_{ce}\mu_{ce}}{(1+\sqrt{1-\alpha_{ec}\alpha_{ce}})^2} = 1,408 \text{ (31.5 db)}$$

$$\text{Reverse power amplification} = \frac{\alpha_{ec}\mu_{ec}}{(1+\sqrt{1-\alpha_{ec}\alpha_{ce}})^2} = 184 \times 10^{-6}$$

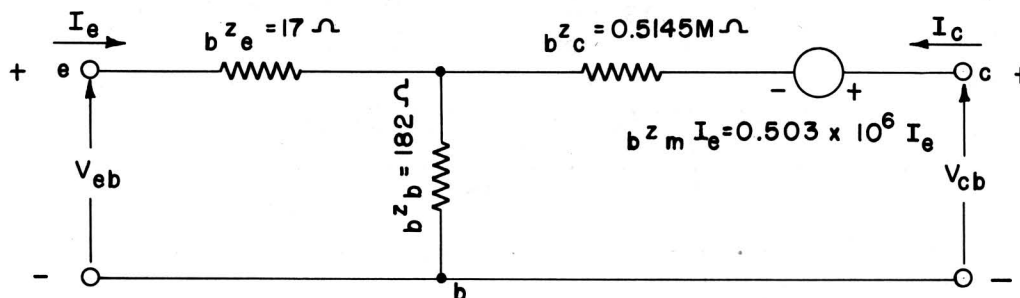
The one-generator equivalent circuit can be obtained by using the (2L)→(5L) transformation equations of Section II. Thus:

# Application of Linear Active Four-Terminal Network Theory to Transistors

$${}_b z_e = z_{eeb} - z_{ecb} = 17 \text{ ohms} \quad {}_b z_c = z_{ccb} - z_{ecb} = 0.5145 \text{ megohm}$$

$${}_b z_b = z_{ecb} = 182 \text{ ohms} \quad {}_b z_m = z_{ceb} - z_{ecb} = 0.503 \text{ megohm}$$

so that the equivalent circuit (5L) of Section II is:



If the common-emitter one-generator equivalent circuit is desired, the (5L)-(4L) transformation equations of Section II are used. Thus:

$$V_{be} = -V_{eb}$$

$$V_{ce} = V_{cb} - V_{eb}$$

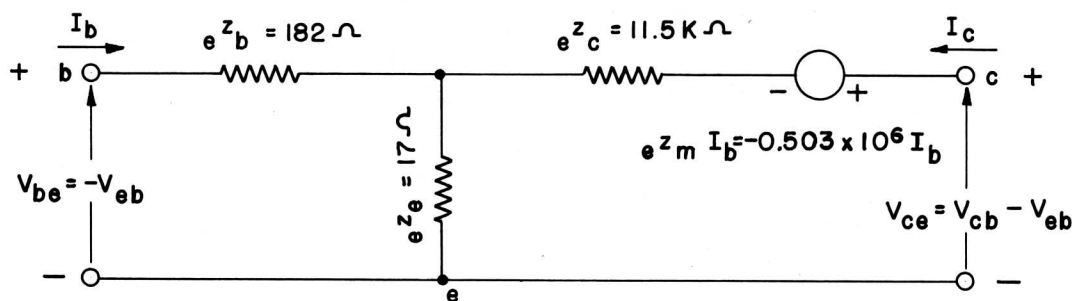
$${}_e z_b = {}_b z_b = 182 \text{ ohms}$$

$${}_e z_c = {}_b z_c - {}_b z_m = 11.5 \text{ K ohms}$$

$${}_e z_e = {}_b z_e = 17 \text{ ohms}$$

$${}_e z_m = -{}_b z_m = -0.503 \text{ megohm}$$

so that the equivalent circuit (4L) of Section II is:



If the common-collector one-generator equivalent circuit is desired, the (5L)-(6L) transformation equations of Section II are used. Thus:

$$V_{bc} = -V_{cb}$$

$$V_{ec} = V_{eb} - V_{cb}$$

$${}_c z_b = {}_b z_b + {}_b z_m = 0.503 \text{ megohm}$$

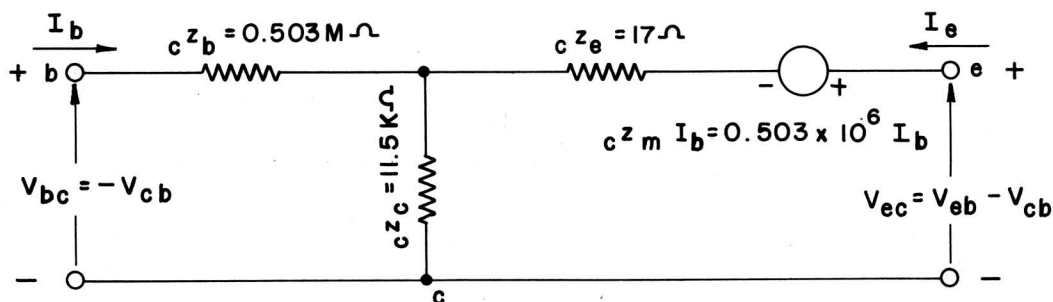
$${}_c z_e = {}_b z_e = 17 \text{ ohms}$$

$${}_c z_c = {}_b z_c - {}_b z_m = 11.5 \text{ K ohms}$$

$${}_c z_m = {}_b z_m = 0.503 \text{ megohm}$$

so that the equivalent circuit (6L) of Section II is:





The amplification factors and the circuit amplifications for the common-emitter and the common-collector circuits will of course be different from those values computed above for the common-base circuit. The appropriate equations can be used to compute the new values.

If a nodal-derived equivalent circuit is desired, the transformation equations of Section 1C must be used. Thus, suppose that the common-emitter one-generator equivalent circuit (4N) of Section 1I is desired. There are several different successive transformations that can be employed to arrive at circuit (4N). The following successive transformation will be employed: (2L) → (1L) → (1N) → (4N).

Thus, (2L) → (1L)

$$V_{be} = -V_{eb}$$

$$V_{ce} = V_{cb} - V_{eb}$$

$$z_{bbe} = z_{eeb} = 199 \text{ ohms}$$

$$z_{cbe} = z_{eeb} - z_{ceb} = -0.503 \times 10^6 \text{ ohms}$$

$$z_{bce} = z_{eeb} - z_{ecb} = 17 \text{ ohms}$$

$$z_{cce} = z_{eeb} - z_{ecb} - z_{ceb} + z_{ccb} = 11.5 \text{ K ohms}$$

And (1L) → (1N)

$$\Delta_z = z_{bbe}z_{cce} - z_{bce}z_{cbe} = 10.84 \times 10^6$$

$$y_{bbe} = z_{cce}/\Delta_z = 1.06 \times 10^{-8} \text{ mho}$$

$$y_{bce} = -z_{bce}/\Delta_z = -1.568 \times 10^{-8} \text{ mho}$$

$$y_{cbe} = -z_{cbe}/\Delta_z = 0.0464 \text{ mho}$$

$$y_{cce} = z_{bbe}/\Delta_z = 18.37 \times 10^{-8} \text{ mho}$$

Finally, (1N) → (4N)

$$e y_{be} = y_{bbe} + y_{bce} = 1.059 \times 10^{-8} \text{ mho}$$

$$1/e y_{be} = 944 \text{ ohms}$$

$$e y_{bc} = -y_{bce} = 1.568 \times 10^{-8} \text{ mho}$$

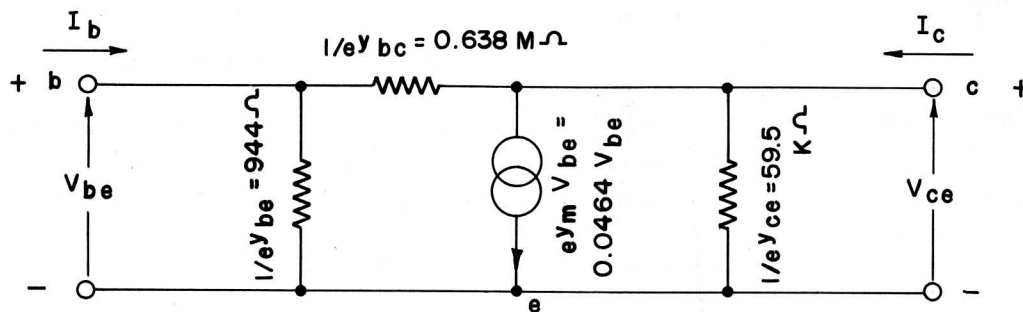
$$1/e y_{bc} = 0.638 \text{ megohm}$$

$$e y_{ce} = y_{cce} + y_{cbe} = 16.8 \times 10^{-8} \text{ mho}$$

$$1/e y_{ce} = 59.5 \text{ K ohms}$$

$$e y_m = y_{cbe} - y_{bce} = 0.0464 \text{ mho}$$

so that the equivalent circuit (4N) is:



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### References

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